

1/14

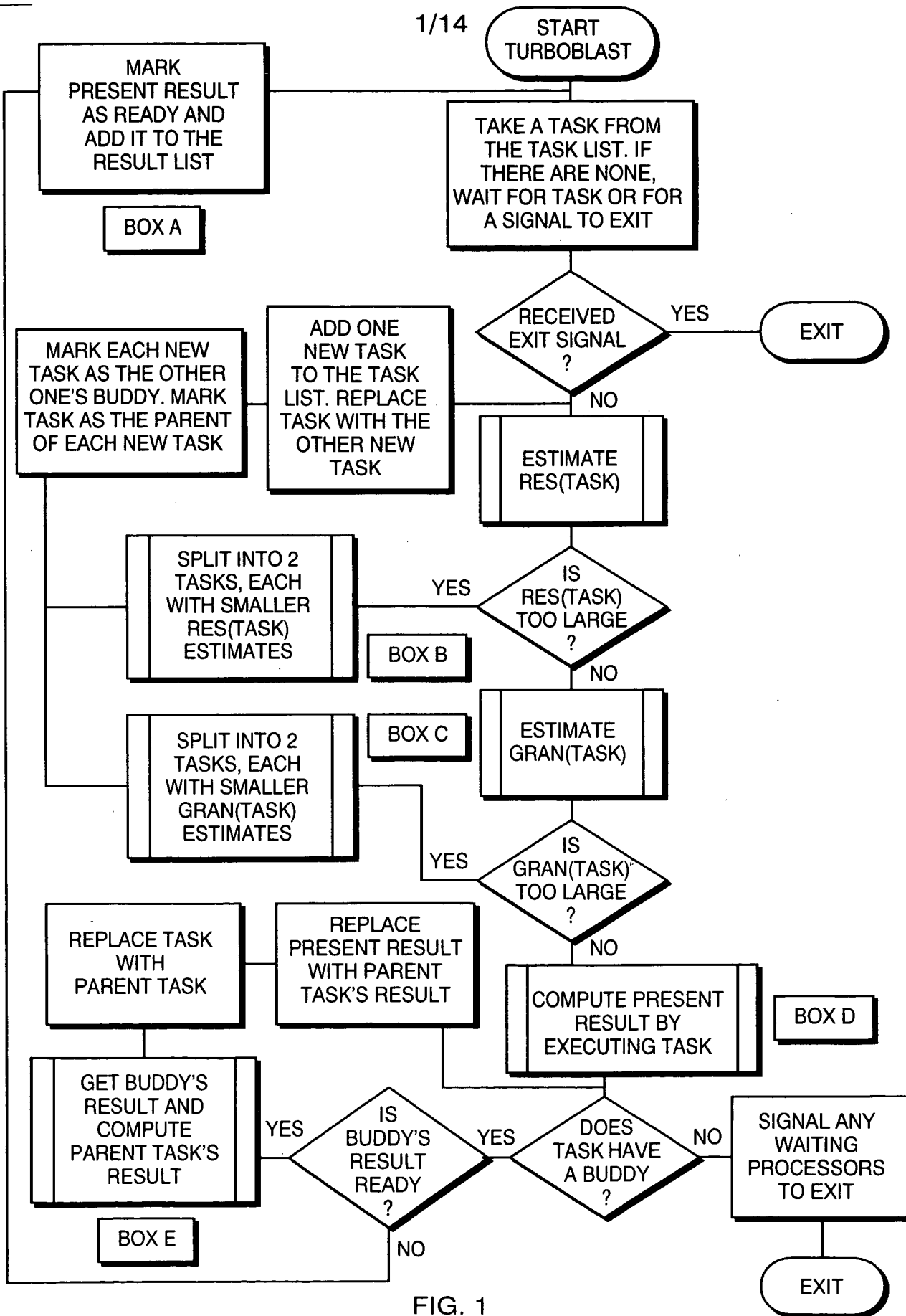


FIG. 1

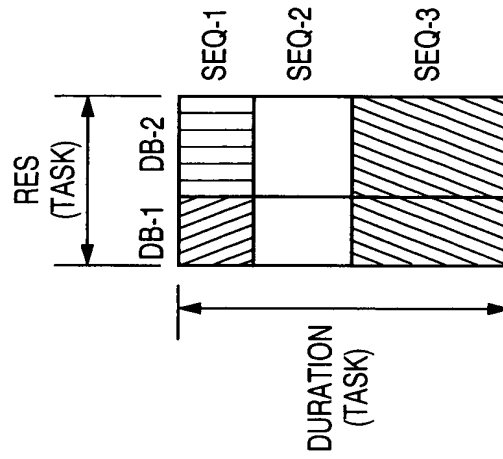


FIG. 2B

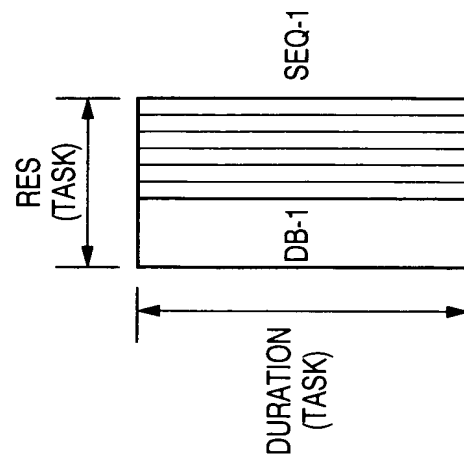
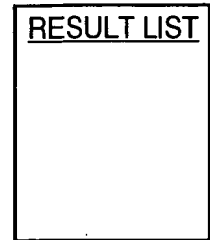
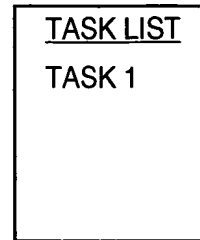
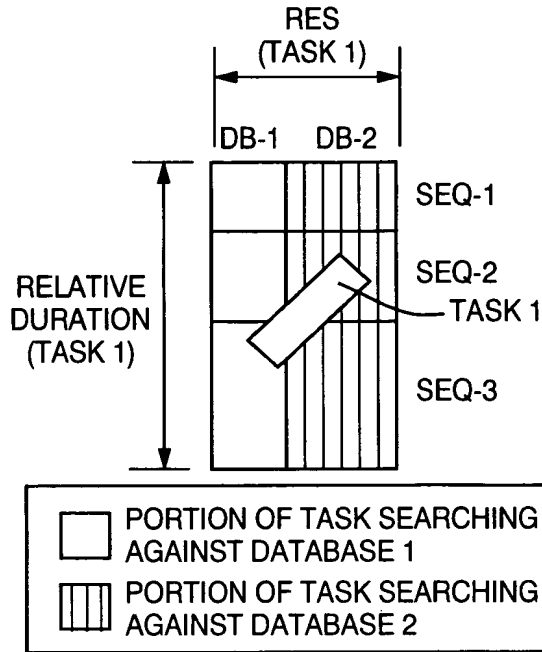


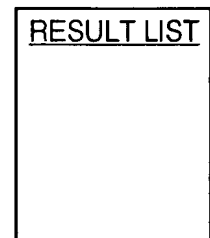
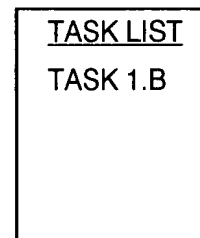
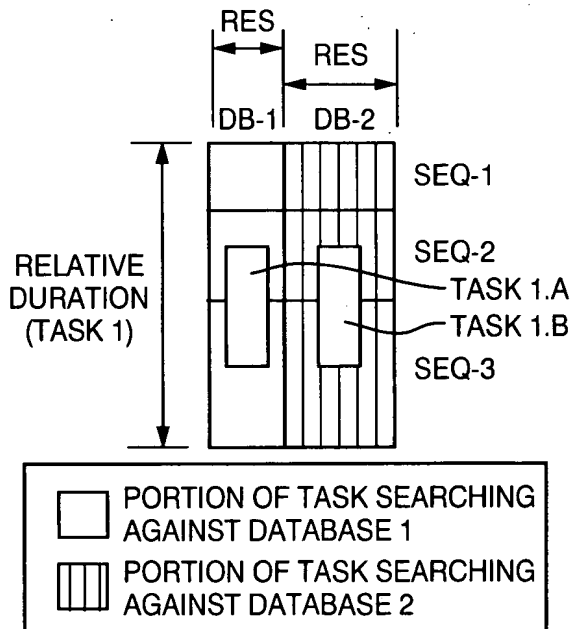
FIG. 2A





PROCESSOR 1: CURRENTLY IDLE
PROCESSOR 2: CURRENTLY IDLE

FIG. 3A



PROCESSOR 1: DONE DIVIDING TASK 1.B.
ABOUT TO WORK ON TASK 1.A
PROCESSOR 2: CURRENTLY IDLE. ABOUT
TO WORK ON TASK 1.B

FIG. 3B

4/14

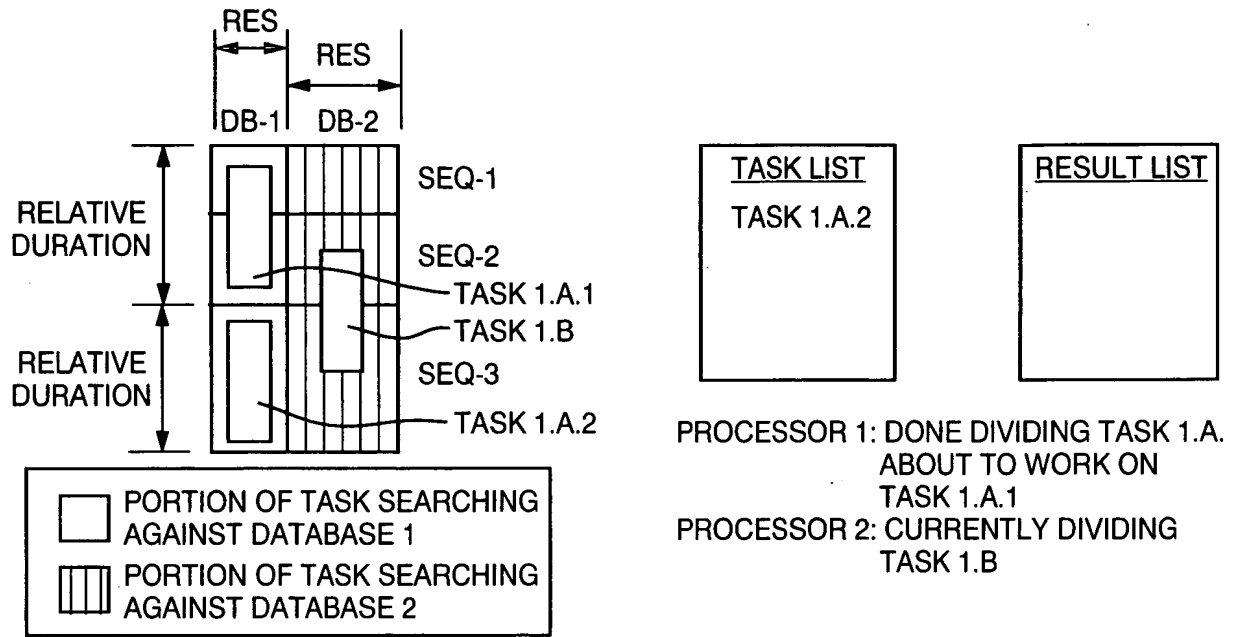


FIG. 3C

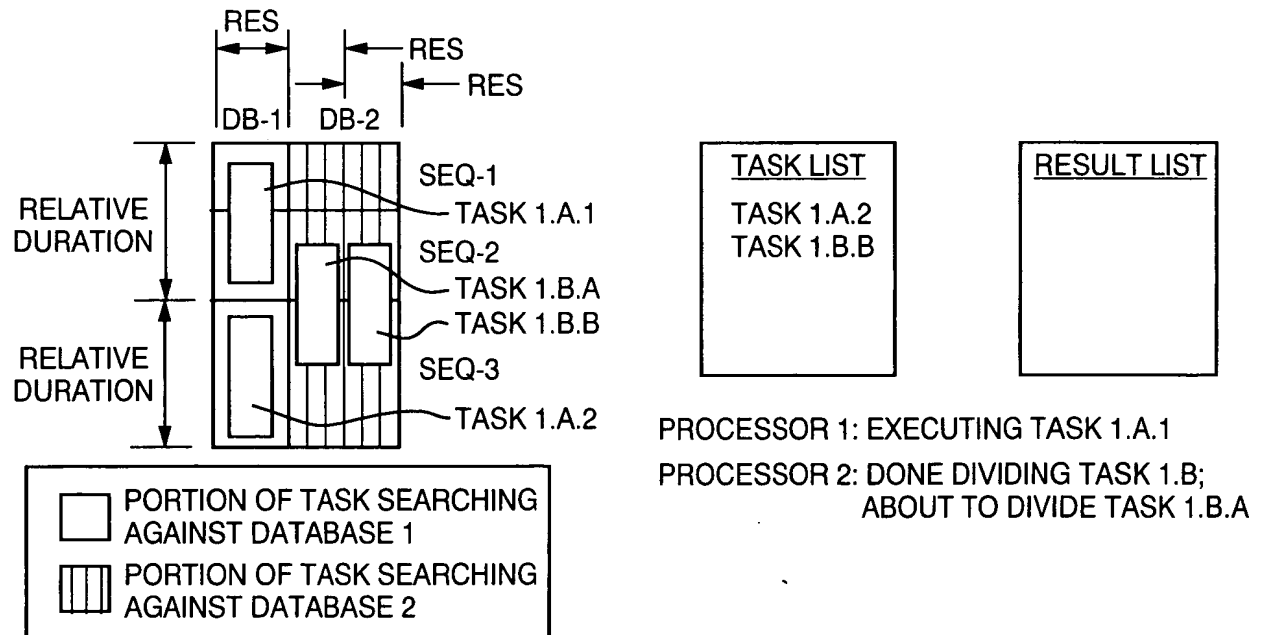


FIG. 3D

5/14

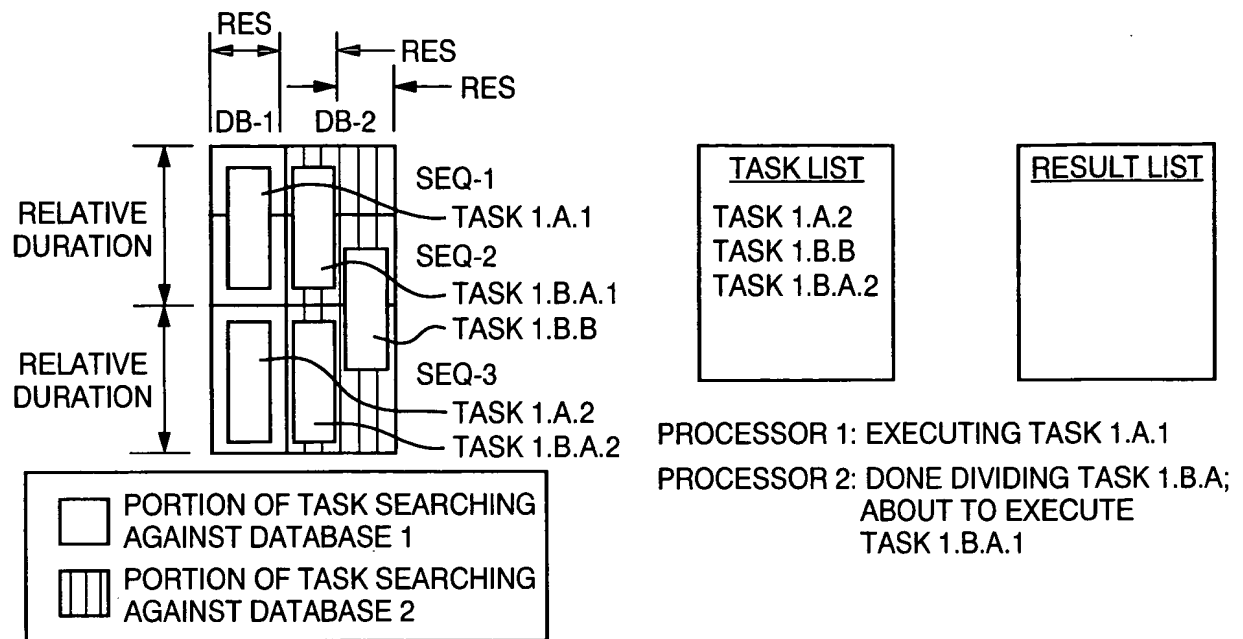


FIG. 3E

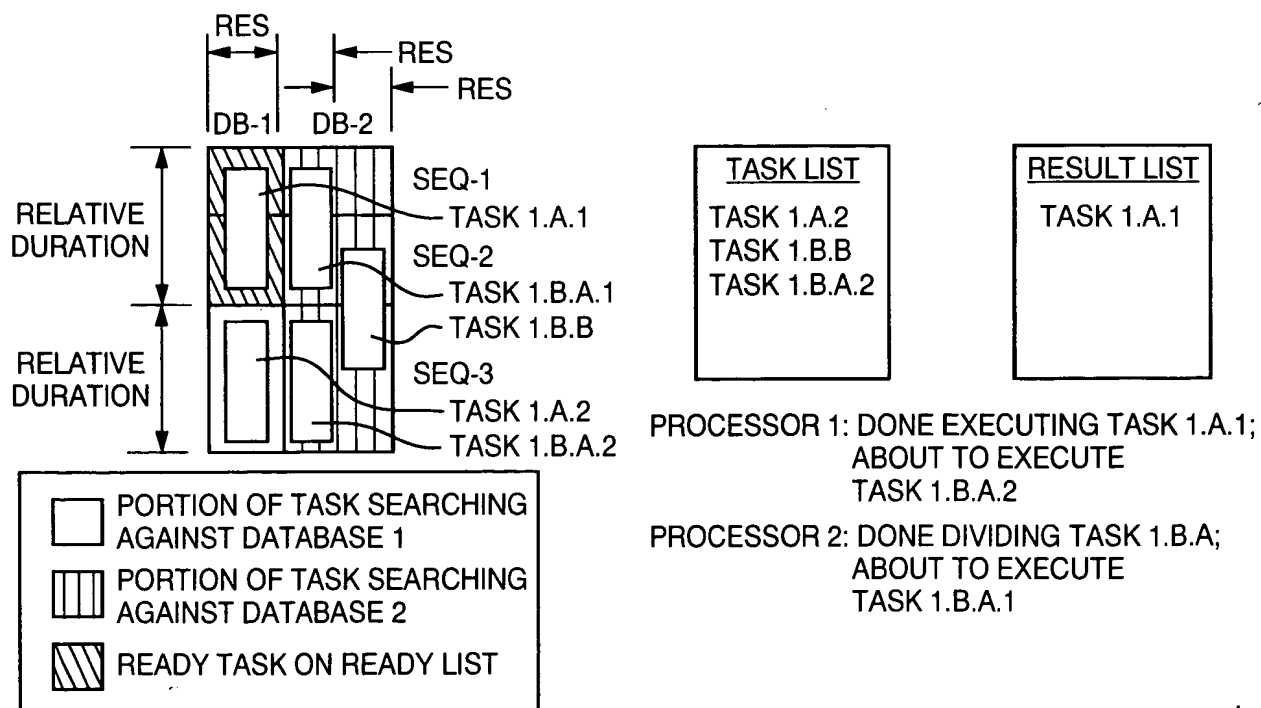
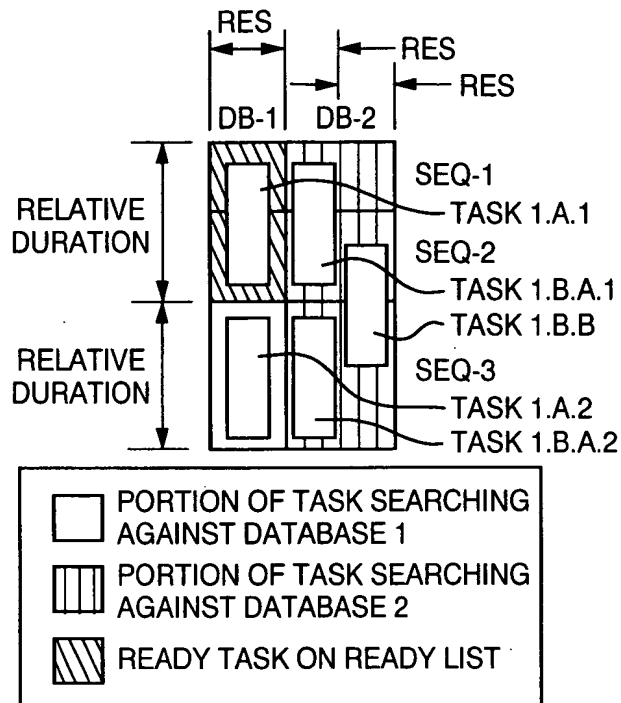


FIG. 3F



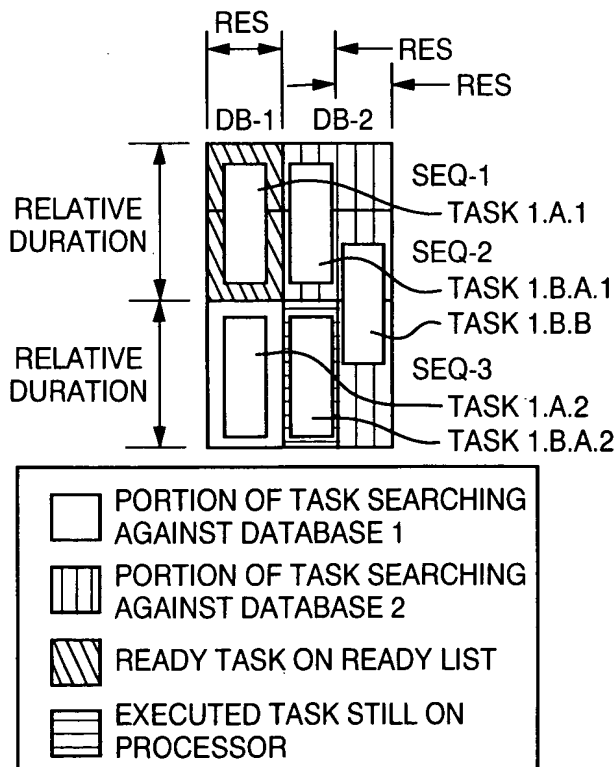
| TASK LIST |
|------------|
| TASK 1.A.2 |
| TASK 1.B.B |

| RESULT LIST |
|--------------|
| TASK 1.A.1 |
| TASK 1.B.A.1 |

PROCESSOR 1: EXECUTING TASK 1.B.A.2

PROCESSOR 2: DONE EXECUTING TASK 1.B.A.1; ABOUT TO EXECUTE TASK 1.A.2

FIG. 3G



| TASK LIST |
|------------|
| TASK 1.B.B |

| RESULT LIST |
|--------------|
| TASK 1.A.1 |
| TASK 1.B.A.1 |

PROCESSOR 1: DONE EXECUTING TASK 1.B.A.2; ABOUT TO MERGE WITH RESULT OF TASK 1.B.A.1

PROCESSOR 2: EXECUTING TASK 1.A.2

FIG. 3H

7/14

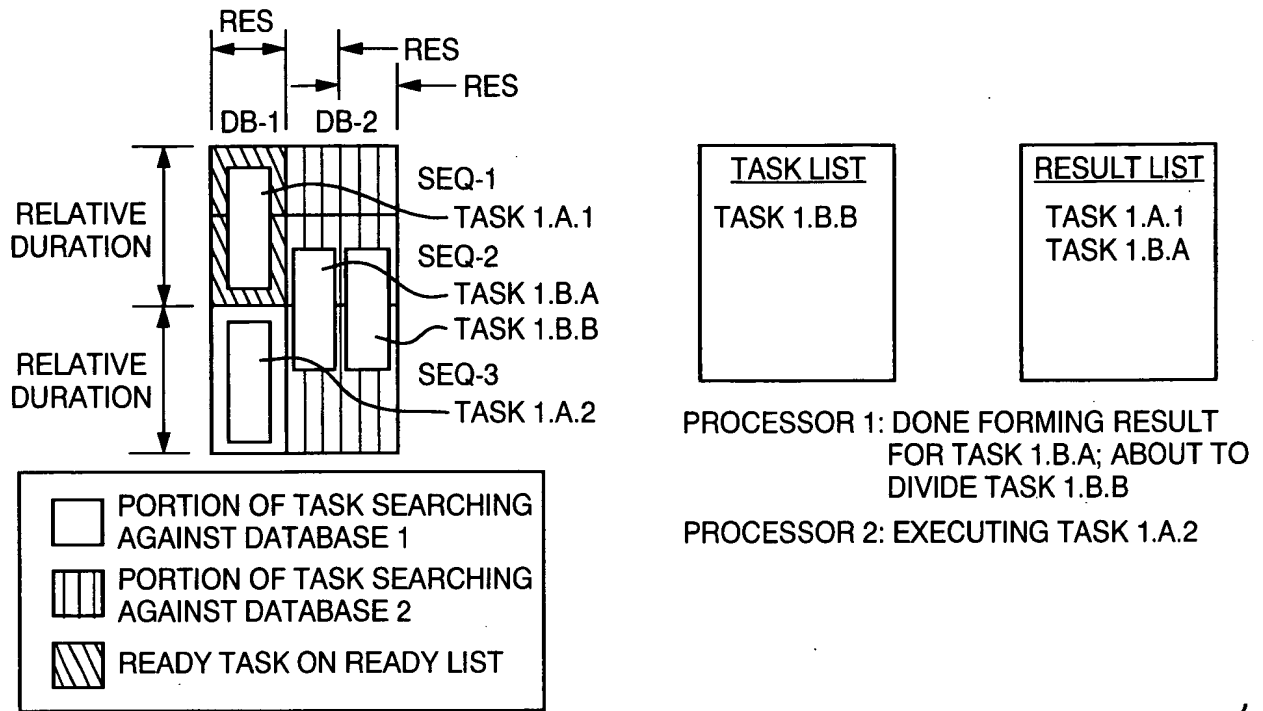


FIG. 3I

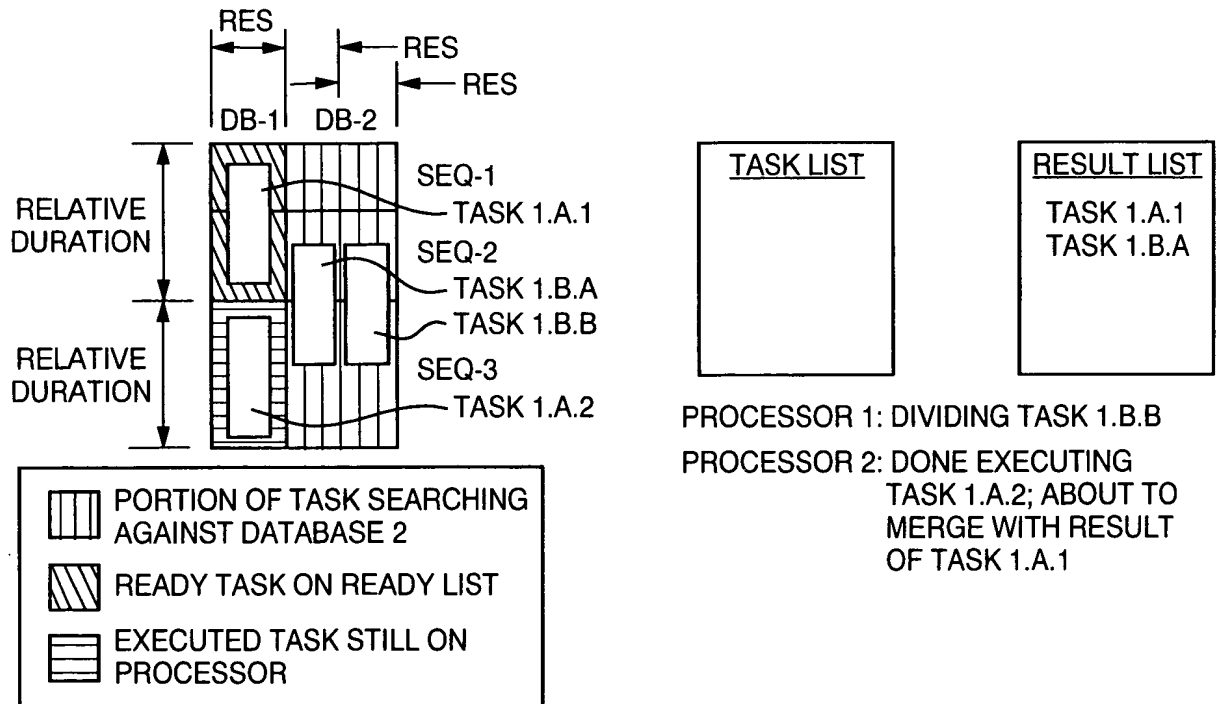
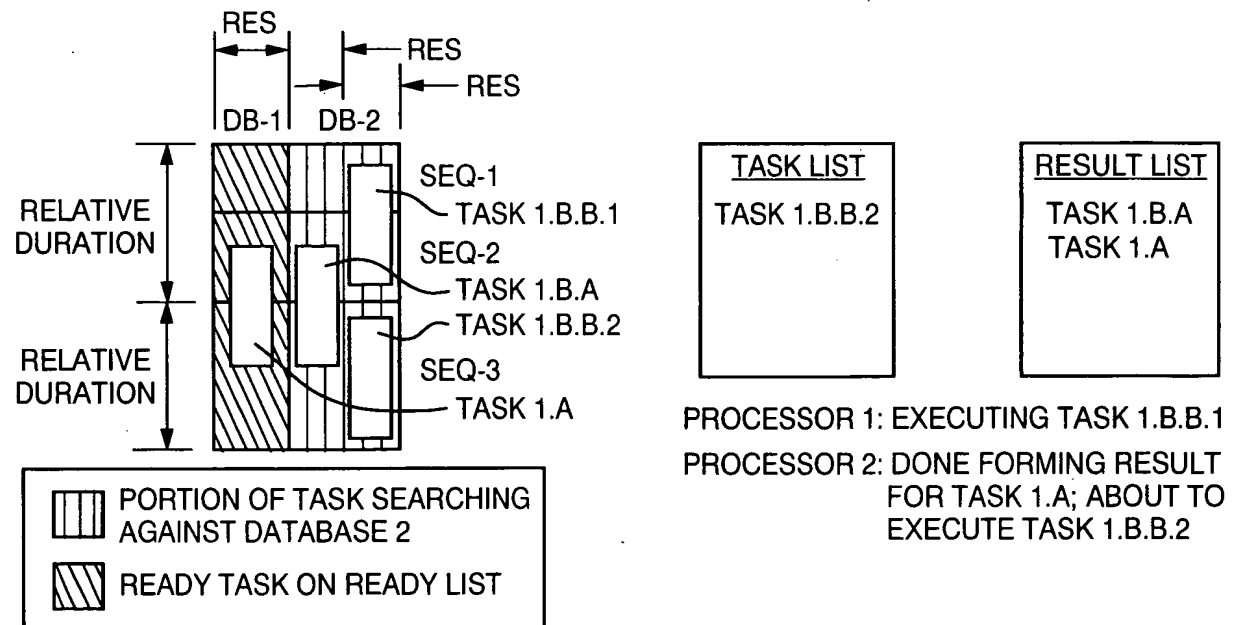
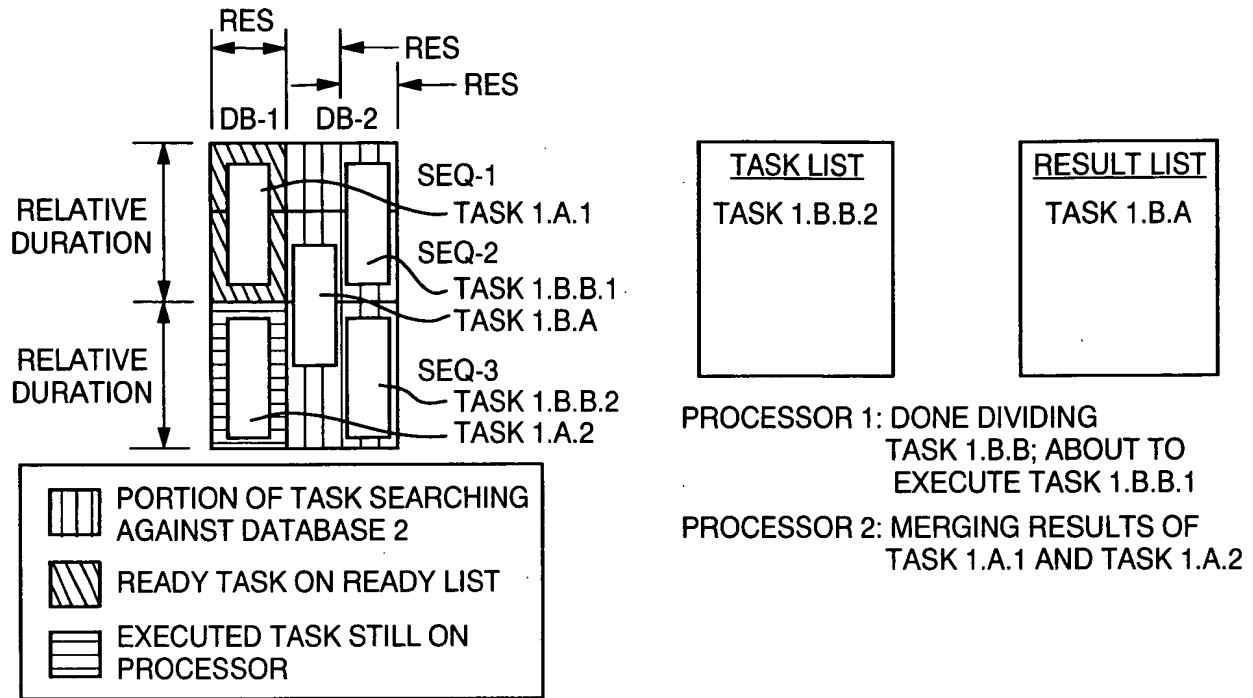


FIG. 3J

8/14



9/14

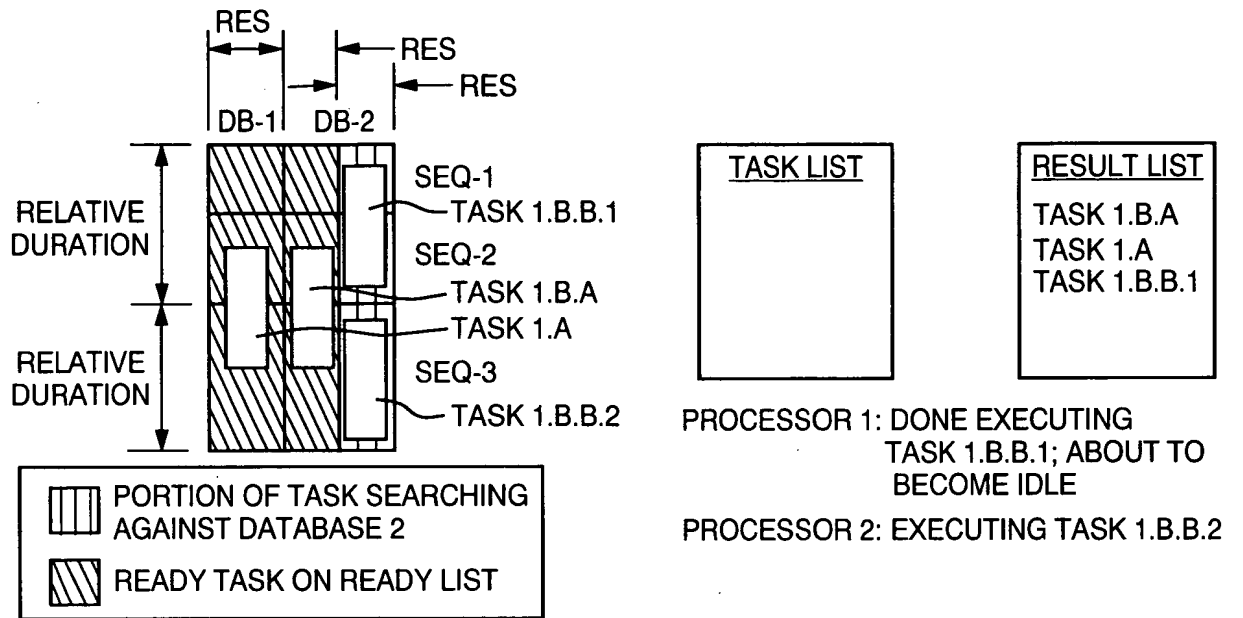


FIG. 3M

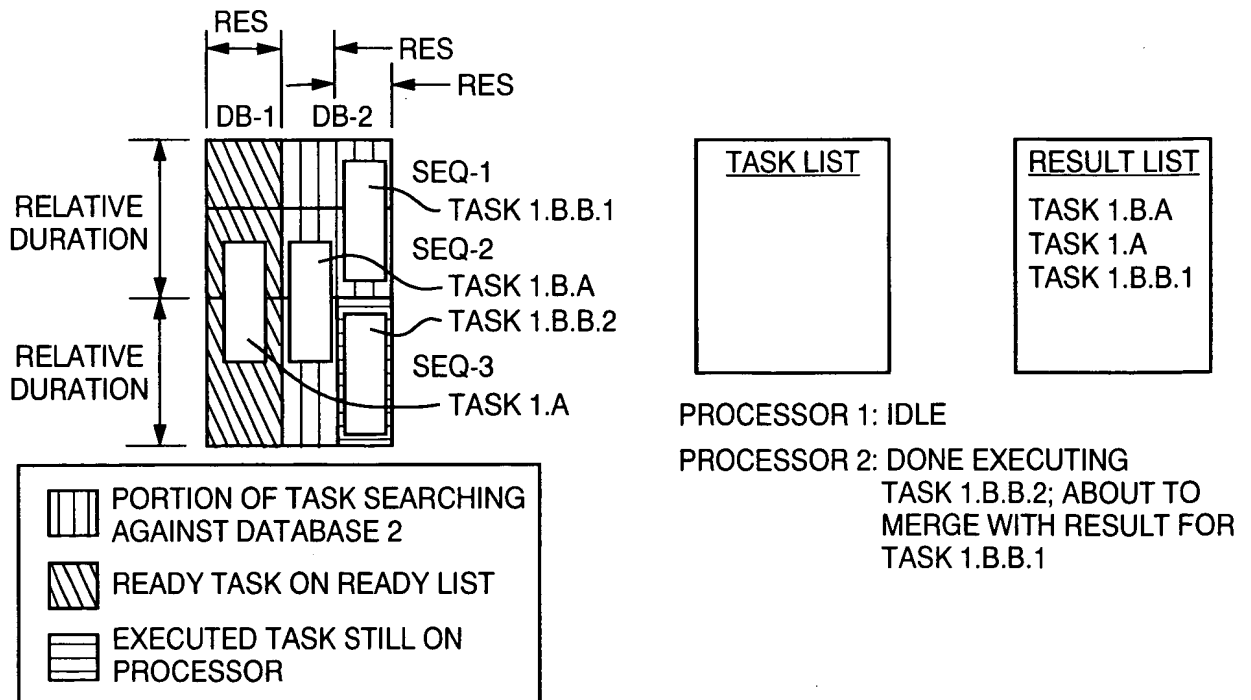


FIG. 3N

10/14

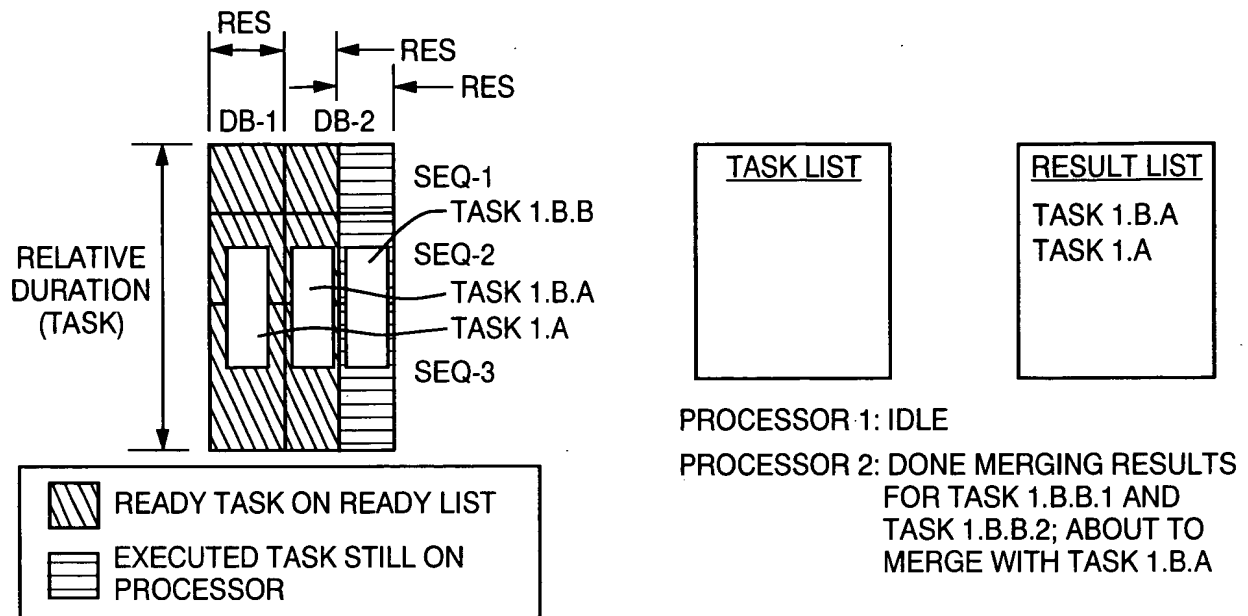


FIG. 30

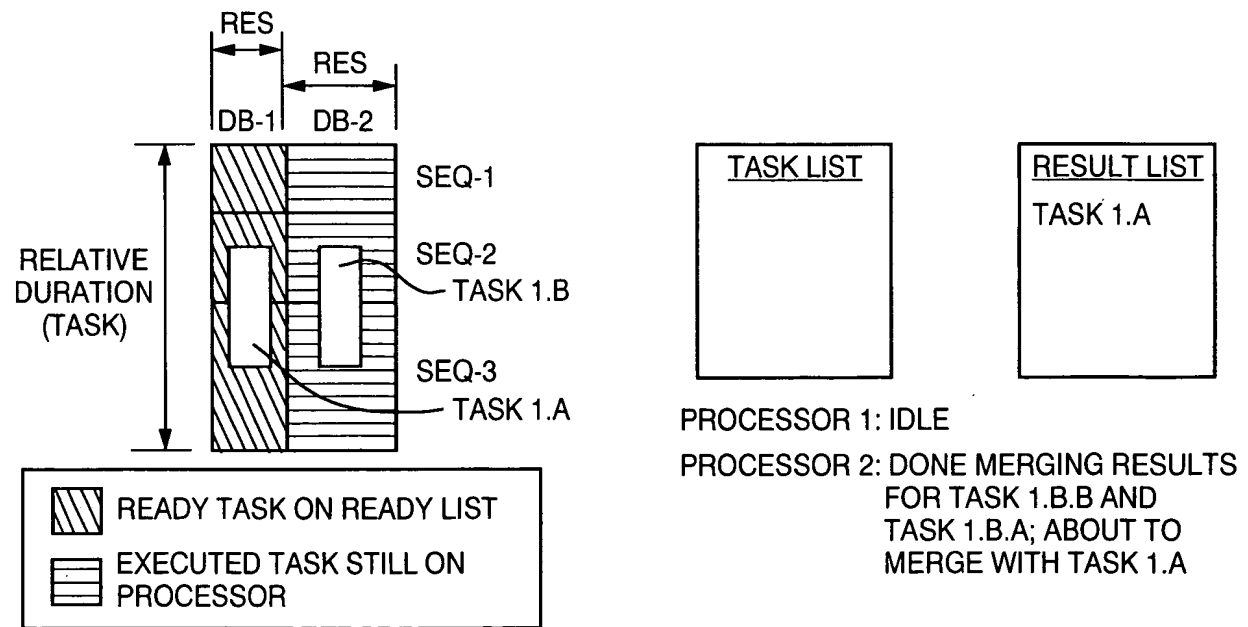


FIG. 3P

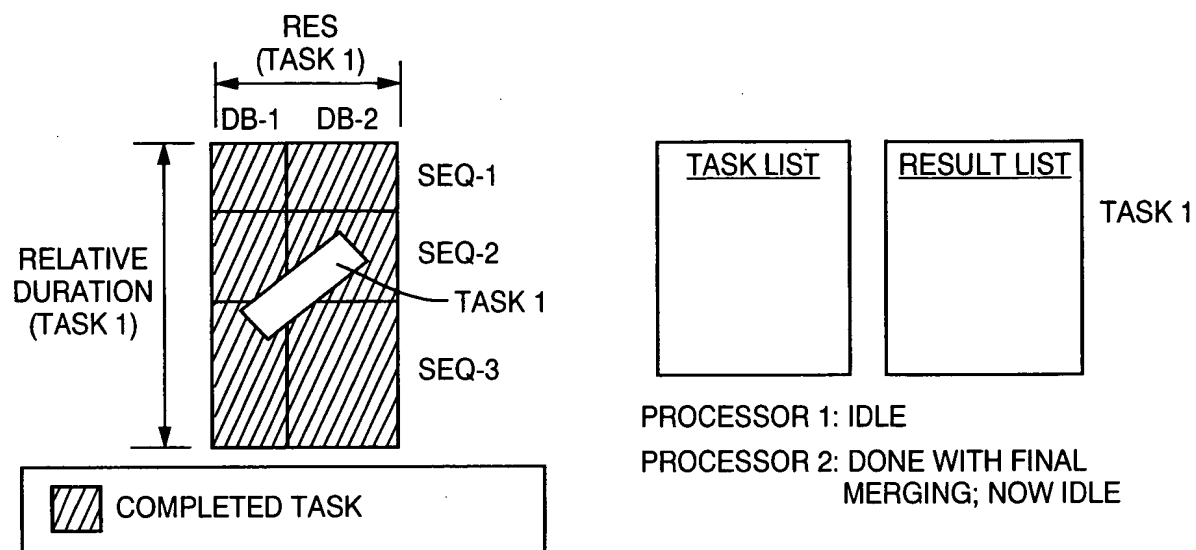


FIG. 3Q

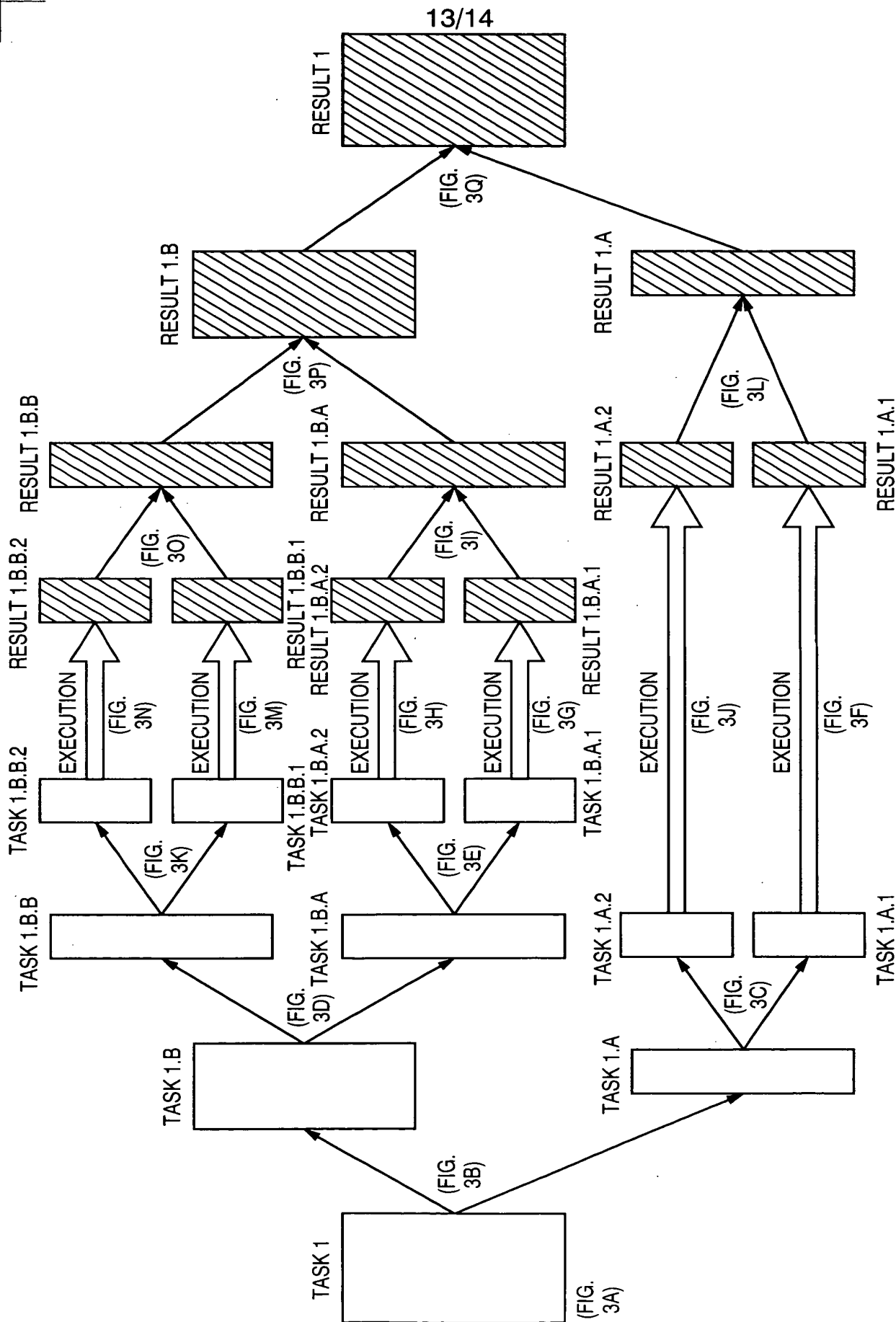


FIG. 5





14/14

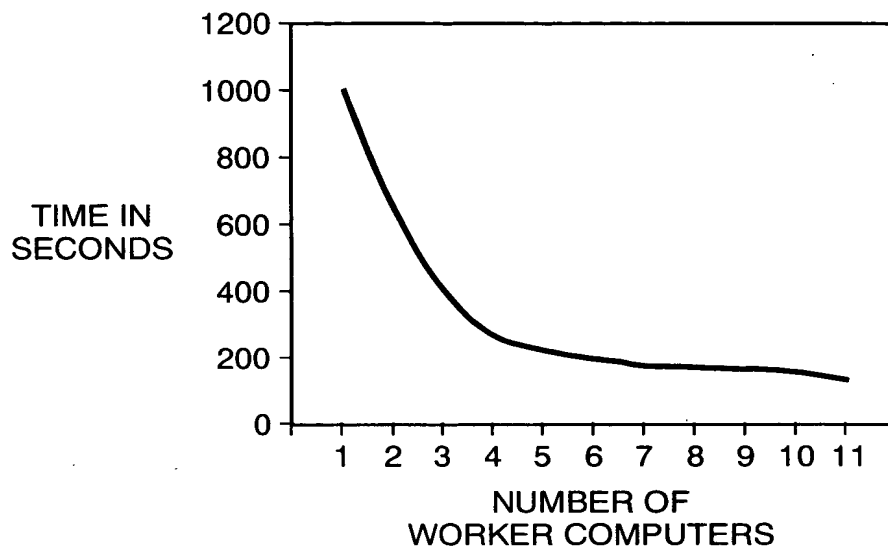


FIG. 6A

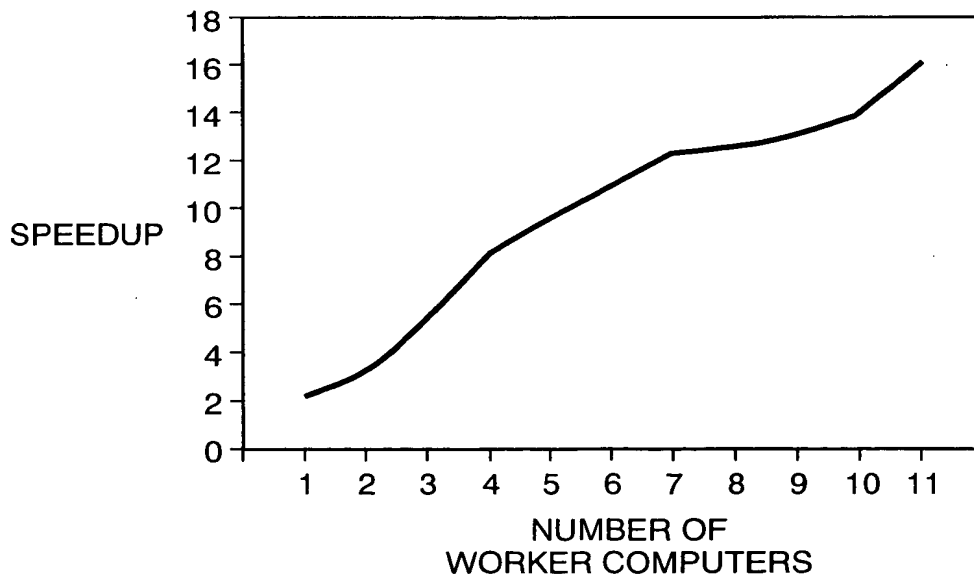


FIG. 6B

